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INFORMATION DISCLOSURE STATEMENT  (Use several sheets if necessary)		APPLICANTS Kazunori ANAZAWA et al.			
		FILING DATE December 1, 2003			
U.S. PATENT DOCUMENTS					
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS
FOREIGN PATENT DOCUMENTS					
	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)					
	S. J. Tans et al., "Room-temperature transistor based on a single carbon nanotube", Nature, vol. 393, pp. 49-51, 1998				
	A. Javey et al., "Carbon Nanotube Transistor Arrays for Multistage Complementary Logic and Ring Oscillators", Nano Letters, vol. 2, pp. 929-939, 2002				
	P.G. Collins et al., "Engineering Carbon Nanotubes and Nanotube Circuits Using Electrical Breakdown", vol. 292, pp. 706-709				
EXAMINER 			DATE CONSIDERED <u>12/04</u>		
Examiner:	Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.				